

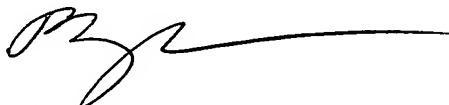
REMARKS

The rejection under 35 USC § 112, 2nd Paragraph has been addressed by clarifying, in each of claims 3, 4, 9, 10, and 17-19, that the test sample is rather than “embodies” a digital circuit. In other words, in these claims, the logic analyzer is analyzing a digital circuit. The recitation that the test sample may be a digital circuit is clearly supported by, for example, lines 21-22 on page 3 of the original specification (“*When received the test data of the test sample, 13, for example, a digital circuit. . .*”).

Since the only change to the claims involves deletion of an objected-to phrase in various dependent claims, the subject matter of all of the claims (including the independent claims) having been indicated as **allowable**, it is respectfully submitted that the amendments **clearly place the application in condition for allowance without raising new issues**, and therefore entry of the amendments and expedited passage of the application to issue is requested.

Should the Examiner nevertheless discover that further changes are needed to place the application in condition for allowance, the Examiner is invited to telephone the undersigned in order to discuss the changes.

Respectfully submitted,
BACON & THOMAS, PLLC



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By: BENJAMIN E. URCIA
Registration No. 33,805

BACON & THOMAS, PLLC
625 Slaters Lane, 4th Floor
Alexandria, Virginia 22314

Telephone: (703) 683-0500